

<b>Notice of References Cited</b>	Application/Control No. 10/693,156		Applicant(s)/Patent Under Reexamination ROBISON, ARCH D.	
	Examiner Eric B. Kiss		Art Unit 2192	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,044,221 A	03-2000	Gupta et al.	717/151
	B	US-			
	C	US-			
	D	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Rajiv Gupta, "Code Optimization as a Side Effect of Instruction Scheduling," Intl. Conf. on High Perf. Computing, pages 370-377, Bangalore, India, Dec. 1997.
	W	Rajiv Gupta, et al., "Resource-Sensitive Profile-Directed Data Flow Analysis for Code Optimization," 30th IEEE/ACM Intl. Symp. on Microarchitecture, pages 358-368, Research Triangle Park, NC, Dec. 1997.
	X	Antony L. Hosking, et al., "Partial Redundancy Elimination for Access Path Expressions," In Proceedings of the International Workshop on Aliasing in Object Oriented Systems (Lisbon, Portugal), 1999, pp. 1-12.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.